	Type	L #	Hits	Search Text	DBs
10	BRS	L10	921	9 and (phase adj (detect$3 comparator comparing comparison)) same (latch$3 flip-flop flipflop flip adj flop holding adj circuit) same (mux multiplex$3 selecting adj circuit selector)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB
11	BRS	L11	1196	9 and (phase adj (detect$3 comparator comparing comparison)) same (latch$3 flip-flop flipflop flip adj flop holding adj circuit) and (latch$3 flip-flop flipflop flip adj flop holding adj circuit) with (mux multiplex$3 selecting adj circuit selector)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB

	Type	L #	Hits	Search Text	DBs
12	BRS	L12	896	9 and (phase adj (detect$3 comparator comparing comparison)) same (latch$3 flip-flop flipflop flip adj flop holding adj circuit)same clock and (latch$3 flip-flop flipflop flip adj flop holding adj circuit) with (mux multiplex$3 selecting adj circuit selector)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB
13	BRS	L13	263	9 and (phase adj (detect$3 comparator comparing comparison)) same (latch$3 flip-flop flipflop flip adj flop holding adj circuit)same clock same (UP DOWN) and (latch$3 flip-flop flipflop flip adj flop holding adj circuit) with (mux multiplex$3 selecting adj circuit selector)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB